# WEST Search History

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DATE: Saturday, February 21, 2004

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| | | DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR | |
| ☐ | L6 | L5 not L1 | 195 |
| ☐ | L5 | L4 and (polish$3 with smooth) | 200 |
| ☐ | L4 | ((die or chip or substrate) with semiconductor) and (smooth with (edge$1 or side$1)) | 1948 |
| ☐ | L3 | ((die or chip) with semiconductor) and (smooth with (edge$1 or side$1)) | 731 |
| ☐ | L2 | 6007916.pn. and (semiconductor or silicon) | 0 |
| ☐ | L1 | (die or chip) with (edge$1 or side$1) with smooth with (polish$3 or grind$3) | 71 |

END OF SEARCH HISTORY

h      e  b      b  cg  b    chh          e                  f  ff  e  ch    e    e  e